

Docket No. 205086US2S/sdc



2188
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AUG 21 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Koichi OTAKE, et al.

SERIAL NO: 09/813,782

GAU: 2184

Technology Center 2100

FILED: March 22, 2001

EXAMINER:

FOR: DATA PROCESSING METHOD USING ERROR-CORRECTING CODE AND AN APPARATUS USING THE SAME METHOD

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Group Art Unit: 2184



STATEMENT OF RELEVANCY

Reference AO (JP 3-232039) on Form 1449:

This document discloses a technique which appears similar to that of the present invention in the respects that it includes an error correction code adding circuit for adding an error correction code to data to be recorded and then recording the data in a semiconductor memory, and an error correction circuit for outputting data whose error has been corrected; however this reference makes no mention of a function of correctly generating and adding an error correction product code.